

[calculatoratoz.com](http://calculatoratoz.com)[unitsconverters.com](http://unitsconverters.com)

# CMOS Design Characteristics Formulas

[Calculators!](#)[Examples!](#)[Conversions!](#)

Bookmark [calculatoratoz.com](http://calculatoratoz.com), [unitsconverters.com](http://unitsconverters.com)

Widest Coverage of Calculators and Growing - **30,000+ Calculators!**

Calculate With a Different Unit for Each Variable - **In built Unit Conversion!**

Widest Collection of Measurements and Units - **250+ Measurements!**

Feel free to SHARE this document with your friends!

[Please leave your feedback here...](#)



# List of 24 CMOS Design Characteristics Formulas

## CMOS Design Characteristics ↗

### 1) Adjacent Capacitance ↗

**fx**

$$C_{adj} = \frac{V_{tm} \cdot C_{gnd}}{V_{agr} - V_{tm}}$$

[Open Calculator ↗](#)

**ex**

$$7.998947\text{pF} = \frac{12.75\text{V} \cdot 2.98\text{pF}}{17.5\text{V} - 12.75\text{V}}$$

### 2) Aggression Driver ↗

**fx**

$$R_{agr} = \frac{R_{vi} \cdot k \cdot (C_{adj} + C_{gnd})}{C_{ga} + C_{adj}}$$

[Open Calculator ↗](#)

**ex**

$$1.123254 = \frac{1.98 \cdot 0.62 \cdot (8\text{pF} + 2.98\text{pF})}{4\text{pF} + 8\text{pF}}$$

### 3) Aggression Time Constant ↗

**fx**

$$\tau_{agr} = k \cdot \tau_{vi}$$

[Open Calculator ↗](#)

**ex**

$$1.2462 = 0.62 \cdot 2.01$$



**4) Aggressor Voltage ↗**

**fx**  $V_{agr} = \frac{V_{tm} \cdot (C_{gnd} + C_{adj})}{C_{adj}}$

**Open Calculator ↗**

**ex**  $17.49938V = \frac{12.75V \cdot (2.98pF + 8pF)}{8pF}$

**5) Branching Effort ↗**

**fx**  $b = \frac{C_{onpath} + C_{offpath}}{C_{onpath}}$

**Open Calculator ↗**

**ex**  $3.8125 = \frac{3.2pF + 9pF}{3.2pF}$

**6) Built-in Potential ↗**

**fx**  $\psi_o = V_t \cdot \ln\left(\frac{N_a \cdot N_d}{n_i^2}\right)$

**Open Calculator ↗**

**ex**  $18.81808V = 0.55V \cdot \ln\left(\frac{1100/m^3 \cdot 1.9e14/m^3}{(17)^2}\right)$

**7) Capacitance Offpath ↗**

**fx**  $C_{offpath} = C_t - C_{onpath}$

**Open Calculator ↗**

**ex**  $9pF = 12.2pF - 3.2pF$



## 8) Capacitance Onpath ↗

**fx**  $C_{\text{onpath}} = C_t - C_{\text{offpath}}$

[Open Calculator ↗](#)

**ex**  $3.2\text{pF} = 12.2\text{pF} - 9\text{pF}$

## 9) Change in Frequency Clock ↗

**fx**  $\Delta f = K_{\text{vco}} \cdot V_{\text{ctrl}}$

[Open Calculator ↗](#)

**ex**  $0.07\text{Hz} = 0.01 \cdot 7\text{V}$

## 10) Ground to Aggression Capacitance ↗

**fx**  $C_{\text{adj}} = \frac{(R_{\text{vi}} \cdot k \cdot C_{\text{gnd}}) - (R_{\text{agr}} \cdot C_{\text{ga}})}{R_{\text{agr}} - R_{\text{vi}} \cdot k}$

[Open Calculator ↗](#)

**ex**  $8.829426\text{pF} = \frac{(1.98 \cdot 0.62 \cdot 2.98\text{pF}) - (1.13 \cdot 4\text{pF})}{1.13 - 1.98 \cdot 0.62}$

## 11) Lock Voltage ↗

**fx**  $V_{\text{lock}} = V_{\text{ctrl}} - V_{\text{offl}}$

[Open Calculator ↗](#)

**ex**  $2\text{V} = 7\text{V} - 5\text{V}$

## 12) Off-Path Capacitance of CMOS ↗

**fx**  $C_{\text{offpath}} = C_{\text{onpath}} \cdot (b - 1)$

[Open Calculator ↗](#)

**ex**  $8.992\text{pF} = 3.2\text{pF} \cdot (3.81 - 1)$



**13) Output Clock Phase**

$$fx \quad \Phi_{out} = 2 \cdot \pi \cdot V_{ctrl} \cdot K_{vco}$$

**Open Calculator**

$$ex \quad 0.439823 = 2 \cdot \pi \cdot 7V \cdot 0.01$$

**14) Static Current**

$$fx \quad i_{static} = \frac{P_{static}}{V_{bc}}$$

**Open Calculator**

$$ex \quad 2.940594mA = \frac{5.94mW}{2.02V}$$

**15) Static Power Dissipation**

$$fx \quad P_{static} = i_{static} \cdot V_{bc}$$

**Open Calculator**

$$ex \quad 5.9994mW = 2.97mA \cdot 2.02V$$

**16) Thermal Voltage of CMOS**

$$fx \quad V_t = \frac{\Psi_0}{\ln\left(\frac{N_a \cdot N_d}{n_i^2}\right)}$$

**Open Calculator**

$$ex \quad 0.549472V = \frac{18.8V}{\ln\left(\frac{1100/m^3 \cdot 1.9e14/m^3}{(17)^2}\right)}$$



**17) Time Constant Ratio of Aggressor to Victim** ↗

$$fx \quad k = \frac{\tau_{agr}}{\tau_{vi}}$$

**Open Calculator** ↗

$$ex \quad 0.616915 = \frac{1.24}{2.01}$$

**18) Total Capacitance Seen by Stage** ↗

$$fx \quad C_t = C_{onpath} + C_{offpath}$$

**Open Calculator** ↗

$$ex \quad 12.2\text{pF} = 3.2\text{pF} + 9\text{pF}$$

**19) VCO Control Voltage** ↗

$$fx \quad V_{ctrl} = V_{lock} + V_{offl}$$

**Open Calculator** ↗

$$ex \quad 7V = 2V + 5V$$

**20) VCO Offset Voltage** ↗

$$fx \quad V_{offl} = V_{ctrl} - V_{lock}$$

**Open Calculator** ↗

$$ex \quad 5V = 7V - 2V$$



## 21) VCO Single Gain Factor ↗

**fx**  $K_{vco} = \frac{\Delta f}{V_{ctrl}}$

[Open Calculator ↗](#)

**ex**  $0.011429 = \frac{0.08\text{Hz}}{7\text{V}}$

## 22) Victim Driver ↗

**fx**  $R_{vi} = \frac{R_{agr} \cdot (C_{ga} + C_{adj})}{k \cdot (C_{adj} + C_{gnd})}$

[Open Calculator ↗](#)

**ex**  $1.991891 = \frac{1.13 \cdot (4\text{pF} + 8\text{pF})}{0.62 \cdot (8\text{pF} + 2.98\text{pF})}$

## 23) Victim Time Constant ↗

**fx**  $\tau_{vi} = \frac{\tau_{agr}}{k}$

[Open Calculator ↗](#)

**ex**  $2 = \frac{1.24}{0.62}$

## 24) Victim Voltage ↗

**fx**  $V_{tm} = \frac{V_{agr} \cdot C_{adj}}{C_{gnd} + C_{adj}}$

[Open Calculator ↗](#)

**ex**  $12.75046\text{V} = \frac{17.5\text{V} \cdot 8\text{pF}}{2.98\text{pF} + 8\text{pF}}$



## Variables Used

- $b$  Branching Effort
- $C_{adj}$  Adjacent Capacitance (*Picofarad*)
- $C_{ga}$  Ground A Capacitance (*Picofarad*)
- $C_{gnd}$  Ground Capacitance (*Picofarad*)
- $C_{offpath}$  Capacitance Offpath (*Picofarad*)
- $C_{onpath}$  Capacitance Onpath (*Picofarad*)
- $C_t$  Total Capacitance in Stage (*Picofarad*)
- $i_{static}$  Static Current (*Milliampere*)
- $k$  Time Constant Ratio
- $K_{vco}$  VCO Gain
- $N_a$  Acceptor Concentration (*1 per Cubic Meter*)
- $N_d$  Donor Concentration (*1 per Cubic Meter*)
- $n_i$  Intrinsic Electron Concentration
- $P_{static}$  Static Power (*Milliwatt*)
- $R_{agr}$  Aggressor Driver
- $R_{vi}$  Victim Driver
- $V_{agr}$  Aggressor Voltage (*Volt*)
- $V_{bc}$  Base Collector Voltage (*Volt*)
- $V_{ctrl}$  VCO Control Voltage (*Volt*)
- $V_{lock}$  Lock Voltage (*Volt*)
- $V_{offl}$  VCO Offset Voltage (*Volt*)



- $V_t$  Thermal Voltage (Volt)
- $V_{tm}$  Victim Voltage (Volt)
- $\Delta f$  Change in Frequency of Clock (Hertz)
- $T_{agr}$  Agression Time Constant
- $T_{vi}$  Victim Time Constant
- $\Phi_{out}$  Output Clock Phase
- $\Psi_o$  Built-in Potential (Volt)



# Constants, Functions, Measurements used

- **Constant:** **pi**, 3.14159265358979323846264338327950288  
*Archimedes' constant*
- **Function:** **In**, **In(Number)**  
*Natural logarithm function (base e)*
- **Measurement:** **Electric Current** in Milliamperere (mA)  
*Electric Current Unit Conversion* ↗
- **Measurement:** **Power** in Milliwatt (mW)  
*Power Unit Conversion* ↗
- **Measurement:** **Frequency** in Hertz (Hz)  
*Frequency Unit Conversion* ↗
- **Measurement:** **Capacitance** in Picofarad (pF)  
*Capacitance Unit Conversion* ↗
- **Measurement:** **Electric Potential** in Volt (V)  
*Electric Potential Unit Conversion* ↗
- **Measurement:** **Carrier Concentration** in 1 per Cubic Meter ( $1/m^3$ )  
*Carrier Concentration Unit Conversion* ↗



## Check other formula lists

- [Array Datapath Subsystem Formulas](#) ↗
- [CMOS Circuit Characteristics Formulas](#) ↗
- [CMOS Delay Characteristics Formulas](#) ↗
- [CMOS Design Characteristics Formulas](#) ↗
- [CMOS Power Metrics Formulas](#) ↗
- [CMOS Special Purpose Subsystem Formulas](#) ↗
- [CMOS Time Characteristics Formulas](#) ↗

Feel free to SHARE this document with your friends!

## PDF Available in

[English](#) [Spanish](#) [French](#) [German](#) [Russian](#) [Italian](#) [Portuguese](#) [Polish](#) [Dutch](#)

12/5/2023 | 4:57:08 AM UTC

[Please leave your feedback here...](#)

